

IN THE CLAIMS:

Claim 1. **(Currently Amended)** An input/output interface, wherein
a logical ~~value is~~ values are expressed respectively by ~~an order~~ orders that
transition edges appear in a plurality of transmission signals transmitting respectively on
a plurality of signal lines.

Claim 2. **(Currently Amended)** The input/output interface according to claim 1,
wherein:

each of said transmission signals ~~include~~ includes a plurality of the transition
edges; and

said logical ~~value is~~ values are expressed by combining the ~~order~~ orders that the
respective transition edges appear in the transmission signals.

Claim 3. **(Currently Amended)** The input/output interface according to claim 2,
wherein:

said transmission signals are pulse signals; and

said logical ~~value is~~ values are expressed by using the ~~order~~ orders that the
transition edges appear in the pulse signals.

Claim 4. **(Currently Amended)** The input/output interface according to claim 3,
wherein